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## APPLICANTS

Sergej B. Gashkov, Moscow, RUSSIAN FEDERATION;  
Alexander E. Andreev, San Jose, CA;  
Aiguo Lu, Cupertino, CA;

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\*\* FOREIGN APPLICATIONS \*\*\*\*\*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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## ADDRESS

Leo J. Peters  
LSI LOGIC CORPORATION  
M/S D-106  
1551 McCarthy Boulevard  
Milpitas ,CA 95035

## TITLE

Optimization of adder based circuit architecture

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